# imall

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### MPPS<sup>™</sup> Miniature Package Power Solutions DUAL 20V NPN SILICON LOW SATURATION SWITCHING TRANSISTOR

#### SUMMARY

 $V_{CEO}=20V; R_{SAT} = 47m\Omega; I_{C}= 4.5A$ 

### DESCRIPTION

Packaged in the innovative 3mm x 2mm MLP (Micro Leaded Package) outline, these new 4<sup>th</sup> generation low saturation dual transistors offer extremely low on state losses making them ideal for use in DC-DC circuits and various driving and power management functions.

Additionally users gain several other key benefits:

#### Performance capability equivalent to much larger packges

Improved circuit efficiency & power levels

PCB area and device placement savings

Lower package height (nom 0.9mm)

**Reduced component count** 

### **FEATURES**

- Low Equivalent On Resistance
- Extremely Low Saturation Voltage (150mV @1A)
- h<sub>FE</sub> characterised up to 6A
- I<sub>C</sub>=4.5A Continuous Collector Current
- 3mm x 2mm MLP

### **APPLICATIONS**

- DC DC Converters
- Charging circuits
- Power switches
- Motor control

#### **ORDERING INFORMATION**

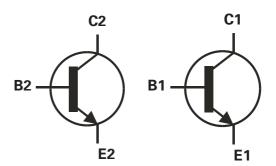
DEVICE	REEL	TAPE WIDTH	QUANTITY PER REEL
ZXTDCM832TA	7''	8mm	3000
ZXTDCM832TC	13′′	8mm	10000

#### DEVICE MARKING DBB

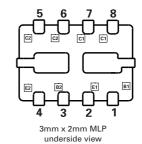
**ISSUE 1 - JUNE 2002** 



3mm x 2mm (Dual die) MLP



### PINOUT



ZETEX



### ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT	
Collector-Base Voltage	V <sub>CBO</sub>	40	V	
Collector-Emitter Voltage	V <sub>CEO</sub>	20	V	
Emitter-Base Voltage	V <sub>EBO</sub>	7.5	V	
Peak Pulse Current	ICM	12	A	
Continuous Collector Current (a)(f)	I <sub>C</sub>	4.5	А	
Continuous Collector Current (b)(f)	I <sub>C</sub>	5	А	
Base Current	IB	1000	mA	
Power Dissipation at TA=25°C (a)(f) Linear Derating Factor	PD	1.5 12	W mW/°C	
Power Dissipation at TA=25°C (b)(f) Linear Derating Factor	PD	2.45 19.6	W mW/°C	
Power Dissipation at TA=25°C (c)(f) Linear Derating Factor	PD	1 8	W mW/°C	
Power Dissipation at TA=25°C (d)(f) Linear Derating Factor	PD	1.13 9	W mW/°C	
Power Dissipation at TA=25°C (d)(g) Linear Derating Factor	PD	1.7 13.6	W mW/°C	
Power Dissipation at TA=25°C (e)(g) Linear Derating Factor	PD	3 24	W mW/°C	
Operating and Storage Temperature Range	Tj:Tstg	-55 to +150	°C	

### THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)(f)	R <sub>0JA</sub>	83.3	°C/W
Junction to Ambient (b)(f)	R <sub>0JA</sub>	51	°C/W
Junction to Ambient (c)(f)	$R_{\theta JA}$	125	°C/W
Junction to Ambient (d)(f)	$R_{\theta JA}$	111	°C/W
Junction to Ambient (d)(g)	$R_{\theta JA}$	73.5	°C/W
Junction to Ambient (e)(g)	$R_{\theta JA}$	41.7	°C/W

Notes

(a) For a dual device surface mounted on 8 sq cm single sided 2oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.

(b) Measured at t<5 secs for a dual device surface mounted on 8 sq cm single sided 2oz copper on FR4 PCB, in still air conditions with all exposed pads attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.</li>
(c) For a dual device surface mounted on 8 sq cm single sided 2oz copper on FR4 PCB, in still air conditions with all exposed pads attached attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
(d) For a dual device surface mounted on 10 sq cm single sided 1oz copper on FR4 PCB, in still air conditions with all exposed pads attached attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
(e) For a dual device surface mounted on 85 sq cm single sided 2oz copper on FR4 PCB, in still air conditions with all exposed pads attached attached. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.

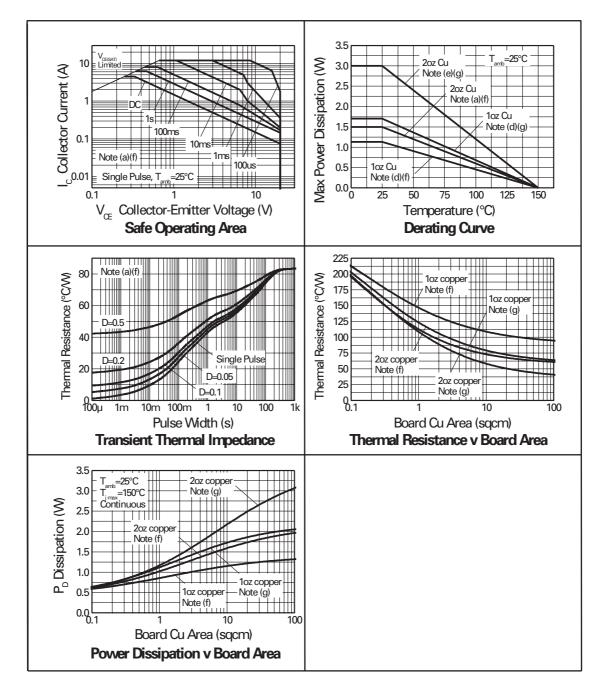
(f) For a dual device with one active die.

(g) For dual device with 2 active die running at equal power.

(h) Repetitive rating - pulse width limited by max junction temperature. Refer to Transient Thermal Impedance graph.

(i) The minimum copper dimensions required for mounting are no smaller than the exposed metal pads on the base of the device as shown in the package dimensions data. The thermal resistance for a dual device mounted on 1.5mm thick FR4 board using minimum copper 1 oz weight, 1mm wide tracks and one half of the device active is Rth = 250°C/W giving a power rating of Ptot = 500mW.





### **TYPICAL CHARACTERISTICS**

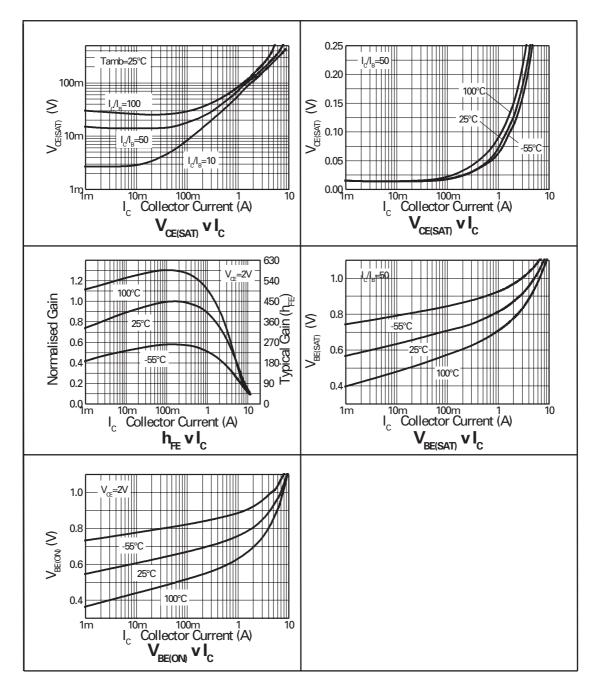
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.		
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	40	100		V	I <sub>C</sub> =100μA		
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	20	27		V	I <sub>C</sub> =10mA*		
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	7.5	8.2		V	I <sub>E</sub> =100μA		
Collector Cut-Off Current	I <sub>CBO</sub>			25	nA	V <sub>CB</sub> =32V		
Emitter Cut-Off Current	I <sub>EBO</sub>			25	nA	V <sub>EB</sub> =6V		
Collector Emitter Cut-Off Current	ICES			25	nA	V <sub>CES</sub> =16V		
Collector-Emitter Saturation	V <sub>CE(sat)</sub>		8	15	mV	I <sub>C</sub> =0.1A, I <sub>B</sub> =10mA*		
Voltage			90	150	mV	I <sub>C</sub> =1A, I <sub>B</sub> =10mA*		
			115	135	mV	I <sub>C</sub> =2A, I <sub>B</sub> =50mA*		
			190	250	mV	I <sub>C</sub> =3A, I <sub>B</sub> =100mA*		
			210	270	mV	I <sub>C</sub> =4.5A, I <sub>B</sub> =125mA*		
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>		0.98	1.05	V	I <sub>C</sub> =4.5A, I <sub>B</sub> =125mA*		
Base-Emitter Turn-On Voltage	V <sub>BE(on)</sub>		0.88	0.95	V	I <sub>C</sub> =4.5A, V <sub>CE</sub> =2V*		
Static Forward Current Transfer Ratio	h <sub>FE</sub>	200 300 200 100	400 450 360 180					
Transition Frequency	f <sub>T</sub>	100	140		MHz	I <sub>C</sub> =50mA, V <sub>CE</sub> =10V f=100MHz		
Output Capacitance	C <sub>obo</sub>		23	30	pF	V <sub>CB</sub> =10V, f=1MHz		
Turn-On Time	t <sub>(on)</sub>		170		ns	V <sub>CC</sub> =10V, I <sub>C</sub> =3A		
Turn-Off Time	t <sub>(off)</sub>		400		ns	I <sub>B1</sub> =I <sub>B2</sub> =10mA		

4

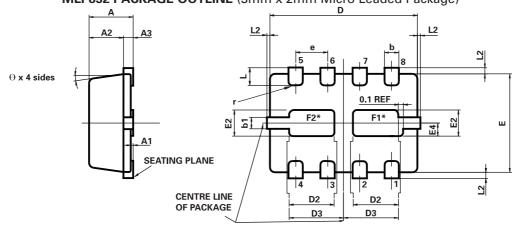
### ELECTRICAL CHARACTERISTICS (at $T_{amb}$ = 25°C unless otherwise stated).

\*Measured under pulsed conditions. Pulse width=300  $\mu s.$  Duty cycle  $\leq 2\%$ 





### **TYPICAL CHARACTERISTICS**



MLP832 PACKAGE OUTLINE (3mm x 2mm Micro Leaded Package)

\*Exposed Flags. Solder connection to improve thermal dissipation is optional. F1 at collector 1 potential

F2 at collector 2 potential

CONTROLLING DIMENSIONS IN MILLIMETRES APPROX. CONVERTED DIMENSIONS IN INCHES

### **MLP832 PACKAGE DIMENSIONS**

	MILLIN	IETRES	INC	HES	MILLIMETRES		IETRES	INCHES	
DIM	MIN.	MAX.	MIN.	MAX.	DIM	MIN.	MAX.	MIN.	MAX.
А	0.80	1.00	0.031	0.039	е	0.65 REF		0.0256 BSC	
A1	0.00	0.05	0.00	0.002	E	2.00	BSC	0.0787	7 BSC
A2	0.65	0.75	0.0255	0.0295	E2	0.43	0.63	0.017	0.0249
A3	0.15	0.25	0.006	0.0098	E4	0.16	0.36	0.006	0.014
b	0.24	0.34	0.009	0.013	L	0.20	0.45	0.0078	0.0157
b1	0.17	0.30	0.0066	0.0118	L2		0.125	0.00	0.005
D	3.00	BSC	0.118 BSC		r	0.075 BSC		0.0029 BSC	
D2	0.82	1.02	0.032	0.040	θ	0°	12°	0°	12°
D3	1.01	1.21	0.0397	0.0476					

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